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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Martinek et al.

Attorney Docket No.: IGT1P369/SH-052

Application No.: 09/520,405

Examiner: Karkhanis, Aashish

Filed: March 8, 2000

Group: 3714

Title: COMPUTERIZED GAMING SYSTEM,  
METHOD AND APPARATUS

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 3, 2007 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

*Chereyce Brown*  
Chereyce Brown

**INFORMATION DISCLOSURE STATEMENT  
BEFORE FINAL ACTION OR NOTICE OF ALLOWANCE  
(37 CFR §§ 1.56 AND 1.97(c))**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, a copy of which is attached, may be material to examination of the above-identified patent application. Applicants submit this reference in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make this citation of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that this reference indeed constitutes prior art.

This Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, or after three months of the filing date of this application, whichever event occurred last, but it is believed before the mailing date of either: (i) a final action under §1.113 or (ii) a notice of allowance under §1.311, whichever occurs first.

Accompanying this Information Disclosure Statement is

- ☐ a statement as specified in 37 CFR 1.97(e); or
- ☒ the fee set forth in 37 CFR 1.17(p).

If fees are due, enclosed is our Check No. 12749 for \$180.00 in payment of the Information Disclosure Statement Fee. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. IGT1P369).

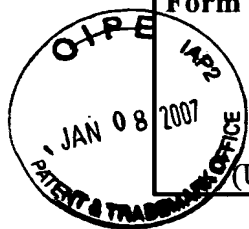
Respectfully submitted,

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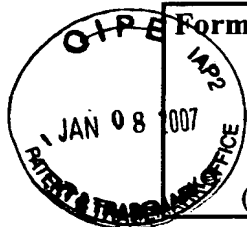


<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No. IGT1P369/SH-052	Application No.: 09/520,405
	Applicant: Martinek et al. Filing Date March 8, 2000	Group 3714

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A1	4,250,563	02/1981	Struger			
	A2	4,293,928	10/1981	Baun			
	A3	4,500,933	02/1985	Chan			
	A4	4,862,355	08/1989	Newman et al.			
	A5	4,972,470	11/1990	Farago			
	A6	5,264,958	11/1993	Johnson			
	A7	5,400,246	03/1995	Wilson et al.			
	A8	5,444,642	08/1995	Montgomery et al.			
	A9	5,508,689	04/1996	Rado et al.			
	A10	5,548,782	08/1996	Michael et al.			
	A11	5,671,351	09/1997	Wild et al.			
	A12	5,688,174	11/1997	Kennedy			
	A13	5,702,303	12/1997	Takemoto et al.			
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	A15	5,935,224	08/1999	Svancarek et al.			
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	A19	6,015,344	01/2000	Kelly et al.			
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	A27	6,269,474	07/2001	Price			
	A28	6,279,124	08/2001	Brouwer et al.			
	A29	6,322,445	11/2001	Miller			
Examiner				Date Considered			

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A30	6,324,605	11/2001	Rafferty et al.			
	A31	6,379,246	04/2002	Dabrowski			
	A32	6,505,087	01/2003	Lucas et al.			

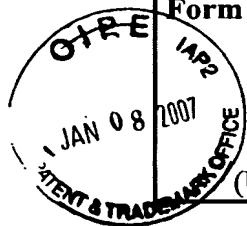
#### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B1	96/14614	05/1996	PCT				
	B2	2 147 773	05/1985	GB				

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C1	RTD USA, <a href="http://www.rtdusa.com">www.rtdusa.com</a> (1998), downloaded from the Internet on Mar. 20, 2003, pp. 1-49
	C2	Mardsen et al., Development of a PC-Windows Based Universal Control System, 5 <sup>th</sup> Intl. Conf. N FACTORY 2000, Apr. 2-4, 1997, Conf. Pub. No. 435, pp. 284-287.
	C3	Paul Virgo, Embedded PC's for the Industrial Marketplace: An Analysis of the STD Bus, WESCON/'93. Conference Record, Sep 28-30, 1993, pp. 621-623.
	C4	Jahn Luke et al., A Commercial Off-the-shelf Based Replacement strategy for aging avionics computers, Aerospace and Electronics Conference, 1998. NAECON 1998, Proceedings of the IEEE 1998 National, Jul 13-17, 1998, pp. 177-181.
	C5	Get Control, Inc. PC-104 DIG-IO-48 Plus, <a href="http://www.getcontrol.com">http://www.getcontrol.com</a> downloaded from the internet on Mar. 20, 2003, p. 1
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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**Other Documents**

	C6	D. Powell et al., GUARDS: a generic upgradeable architecture for real-time dependable systems, Parallel and Distributed Systems, IEEE Transactions on, vol.: 10, Issue: 6, Jun 1999, pp. 580-599.
	C7	Robert, A. Burckle, PC/104 Embedded Modules: The New Systems Components, <a href="http://www.winsystems.com/papers.sys_components.pdf">http://www.winsystems.com/papers.sys_components.pdf</a> downloaded form the Internet on Mar. 20, 2003, pp. 1-3.
	C8	WinSystems, <a href="http://webarchive.org/web/19881212034126/-http://winsystems.com/">http://webarchive.org/web/19881212034126/-http://winsystems.com/</a> (Dec 12, 1998), downloaded form the Internet on Oct. 27, 2003, all pages.
	C9	RTDUSA, < <a href="http://webarchive.org.web/1990422091026/http://rtdusa.com/">www.http://webarchive.org.web/1990422091026/http://rtdusa.com/</a> > (Apr 22, 1999), downloaded form the internet on Oct 27, 2003, all pages.
	C10	WinSystems, <a href="http://www.winsystems.com">www.winsystems.com</a> downloaded form the internet on Apr. 2, 2003, pp. 1-25.
	C11	Jim Blazer, PC/104 Intelligent Data Acquisition, PC/104 Embedded Solutions (Spring 1998), pp. 1-2.
	C12	Robert A. Burckle, STD Bus: Performance without Complexity, <a href="http://www.winsystems.com/papers/stdperformance.pdf">http://www.winsystems.com/papers/stdperformance.pdf</a> (Aug. 1, 2001), pp.1-3.
	C13	Craig Matasumoto, Intel starts preaching about security, EE Times <a href="http://eetimes.com/story/OEG19990121S0014">http://eetimes.com/story/OEG19990121S0014</a> (Jan. 21, 1999), pp. 1-4.
	C14	Copy of PCT Search Report for PCT/US01/07447 mailed on June 12, 2001 (6 pgs.)
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.